



FPGA Design Courses														
Course Code	Course Title	Days	Jan	Feb	Mac	Apr	May	Jun	Jul	Aug	Sept	Oct	Nov	Dec
FPGA13000-11-ILT	Essentials of FPGA Design	1			1- Mac			1-Jun	1-Jul		2-Sep		3-Nov	2-Dec
FPGA23000-11-ILT	Designing for Performance	2		1-2 Feb	9-10 Mac			8-9 June	5-6 July	5-6 Aug	7-8 Sept	4-5 Oct		
FPGA33000-11-ILT	Advanced FPGA Implementation	2		11 - 12 Feb	17-18 Mac				14-15 Jul	7-8 Aug		6-7 Oct		8-9 Dec
FPGA11000-11-ILT	Designing with the PlanAhead™ Analysis and Design Tool	2				5-6 April	3-4 May	14-15 June		24-25 Aug			9-10 Nov	
V5-21000-11-ILT	Designing with the Virtex-5 Family	2				14-15 April		22-23 June						13-14 Dec
S6V6-21000-11-ILT	<b>NEW - Designing with the Spartan-6 and Virtex-6</b>	3			23-25 Mac					10-12 Aug		11-13 Oct		
V6-21000-11-ILT	<b>NEW - Designing with the Virtex-6 Families</b>	2				19-20 April					22-23 Sept		15-16 Nov	
S6-21000-11ILT	<b>NEW - Designing with the Spartan-6 Families</b>	2				21-22 April								22-23 Dec
LOWCOST-10000-8-MOD	Design Techniques for Lower Cost	2						28-29 June						
FPGA16000-11-ILT	ISE Design Tool Flow	1		17-Feb			11-May	30-Jun		15-Aug			18-Nov	
CSP22000-12-ILT (v1.0)	Debugging Techniques Using the ChipScope Pro Tools													
FPGA31000-12-ILT (v1.0)	<b>NEW - Xilinx Partial Reconfiguration Tools and Techniques</b>	2									28-29 Sept			
Languages														
LANG12000-11-ILT	Designing with Verilog	3		22-24 Feb		27-29 April	18-20 May		19-21 Jul	24-26 Aug		19-21 Oct		28-30 Dec
LANG11000-11-ILT	Designing with VHDL	3			29-31 Mac		25-27 May		27-29 Jul			25-27 Oct	23-25 Nov	
Embedded Design Courses														
EMBD21000-11-ILT	Embedded Systems Development	2	Available upon request											
EMBD21000-12-ILT	Embedded Systems Software Development	2	Available upon request											
EMBD33000-10-ILT	Adv. Features & Tech. Embedded Systems Dev.	2	Available upon request											
EMBD22000-11-ILT	Embedded Open-Source Linux Development	2	Available upon request											
DSP Design Courses														
DSP11000-11-ILT	DSP Design Using System Generator	2	Available upon request											
DSP20000-12-ILT (v1.0)	<b>NEW- Essential DSP Implementation Techniques for Xilinx FPGAs</b>	2									20-21 Sept			
CPLD Design Courses														
CPLD13000-9-ILT (v1.0)	Fundamentals of CPLD Design	1	Available upon request											
CPLD23000-9-ILT (v1.0)	Designing for Performance for CPLDs	1	Available upon request											
Connectivity Design														
PCIE28000-92-ILT (v1.0)	Designing a LogiCORE PCI Express System	2	Available upon request											
R12000-10-ILT	Designing with Multi-Gigabit Serial IO	3	Available upon request											
EMAC23000-82-ILT	Designing with Ethernet MAC Controllers	2	Available upon request											

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